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PORTION OF BS 12
FROM FIG. 1

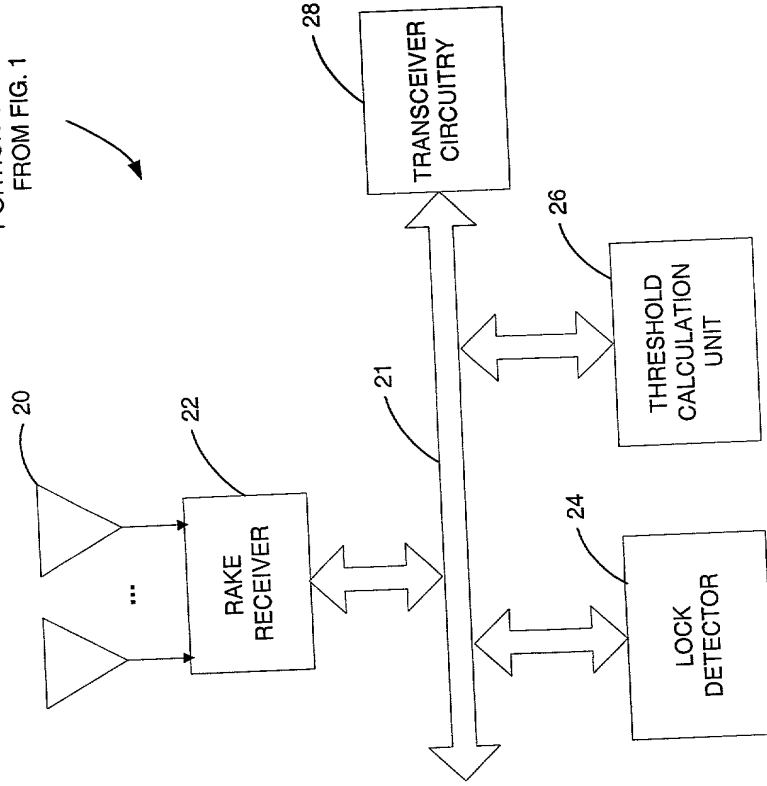


FIG. 2

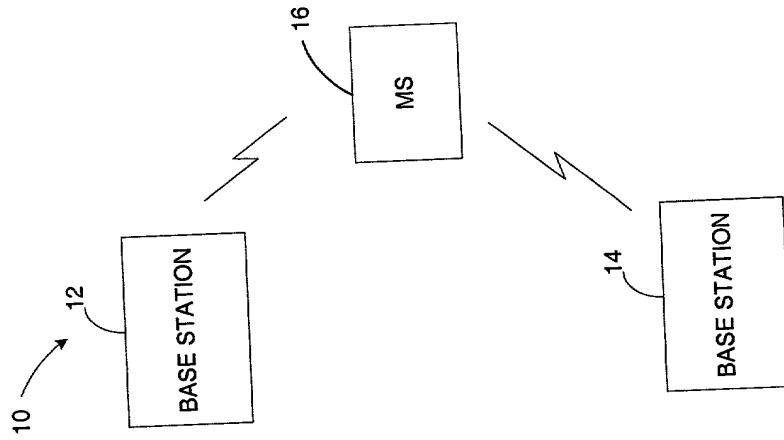


FIG. 1

Diagram illustrating a portion of a lock assembly. A curved arrow points to a component labeled "DETECTOR 24". The text "PORTION OF LOCK" is also present.

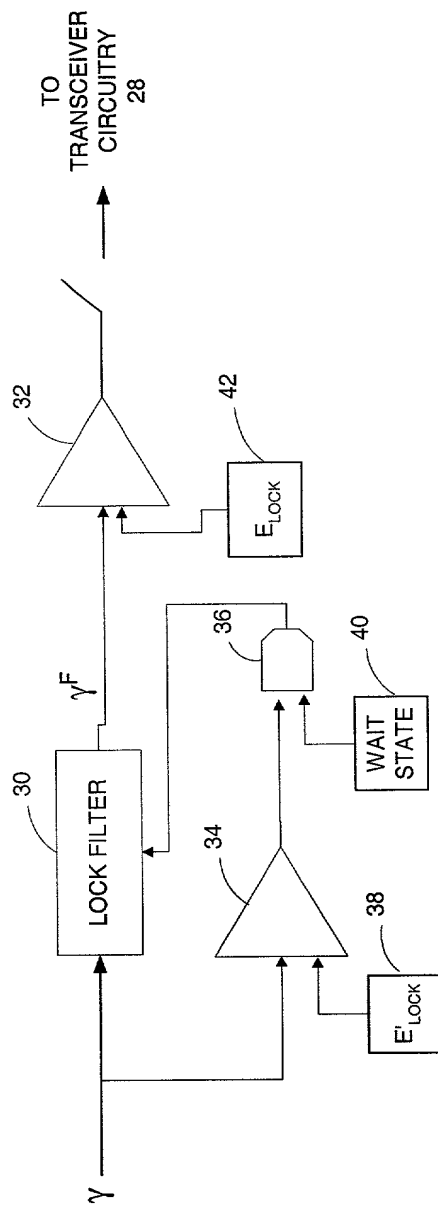


FIG. 3

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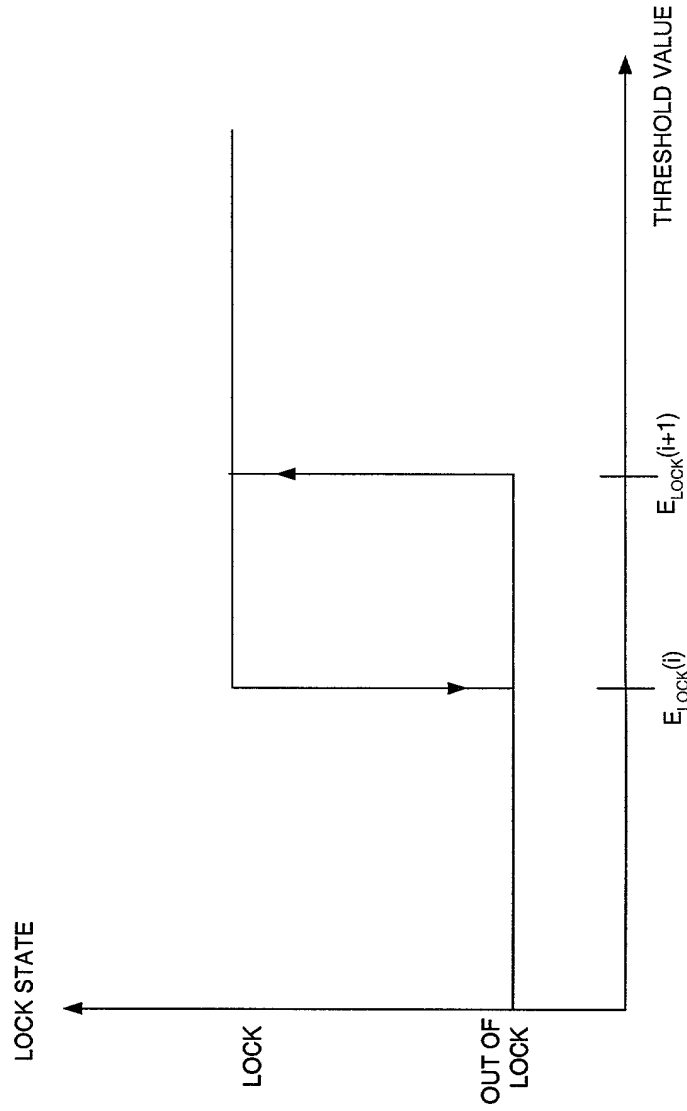


FIG. 4B

FIG. 5 is a graph of signal strength versus time, showing the relationship between the signal strength and the time taken for the system to reach a steady state. The graph illustrates the transient response of the system, with the signal strength increasing over time and eventually settling at a constant value. The time intervals T1 and T2 are indicated, representing the time taken for the signal to reach a certain level and the time taken for it to settle, respectively. The signal strength is denoted by γ and γ^F , and the time intervals are denoted by t_1 , t_2 , t_3 , t_4 , and t_5 .

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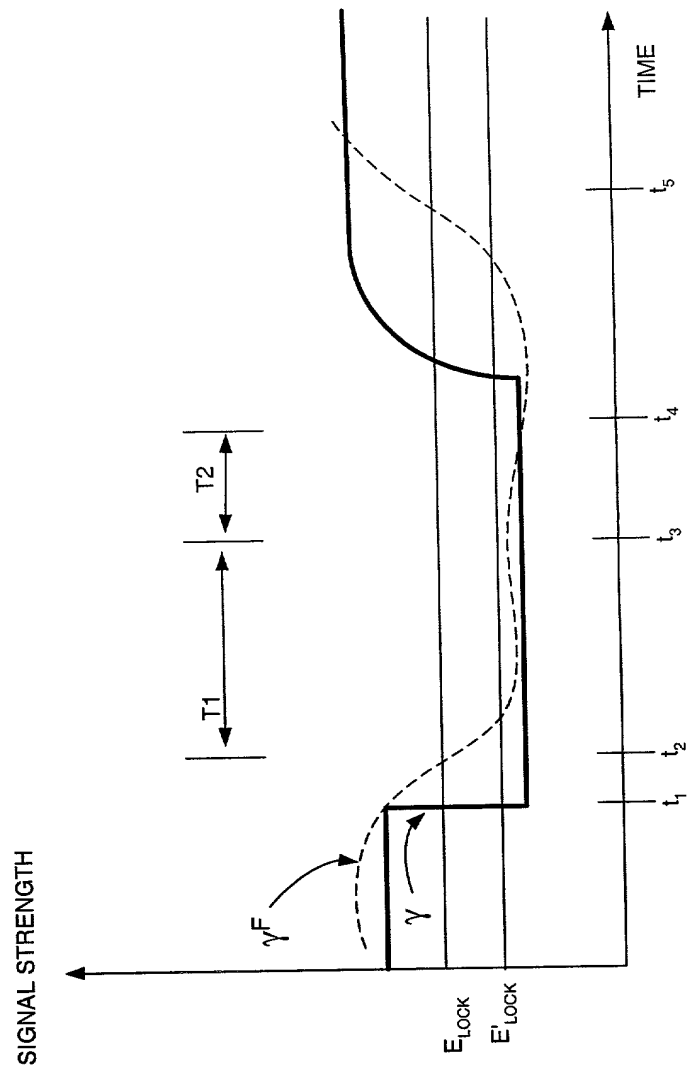


FIG. 5

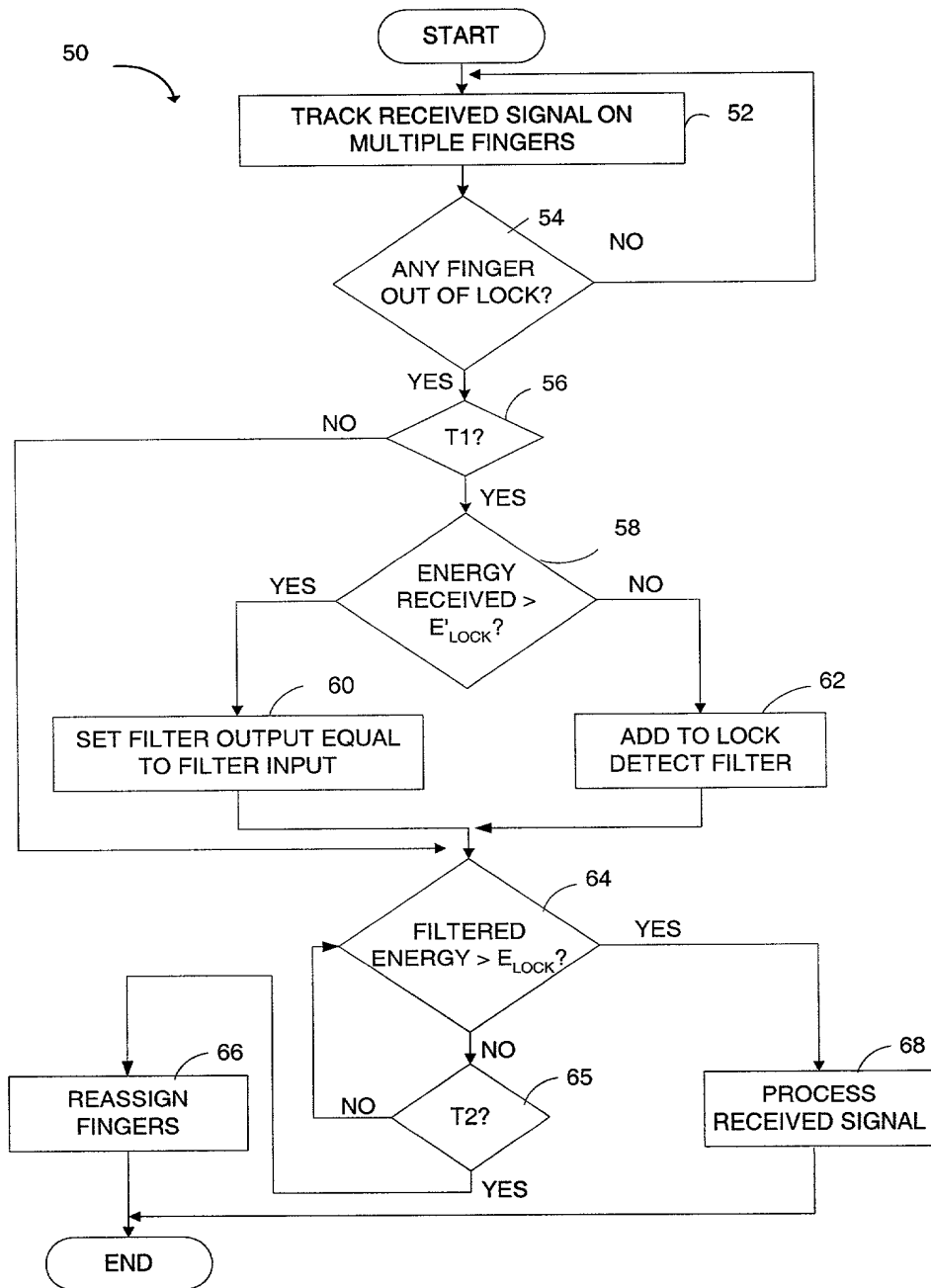


FIG. 6

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100

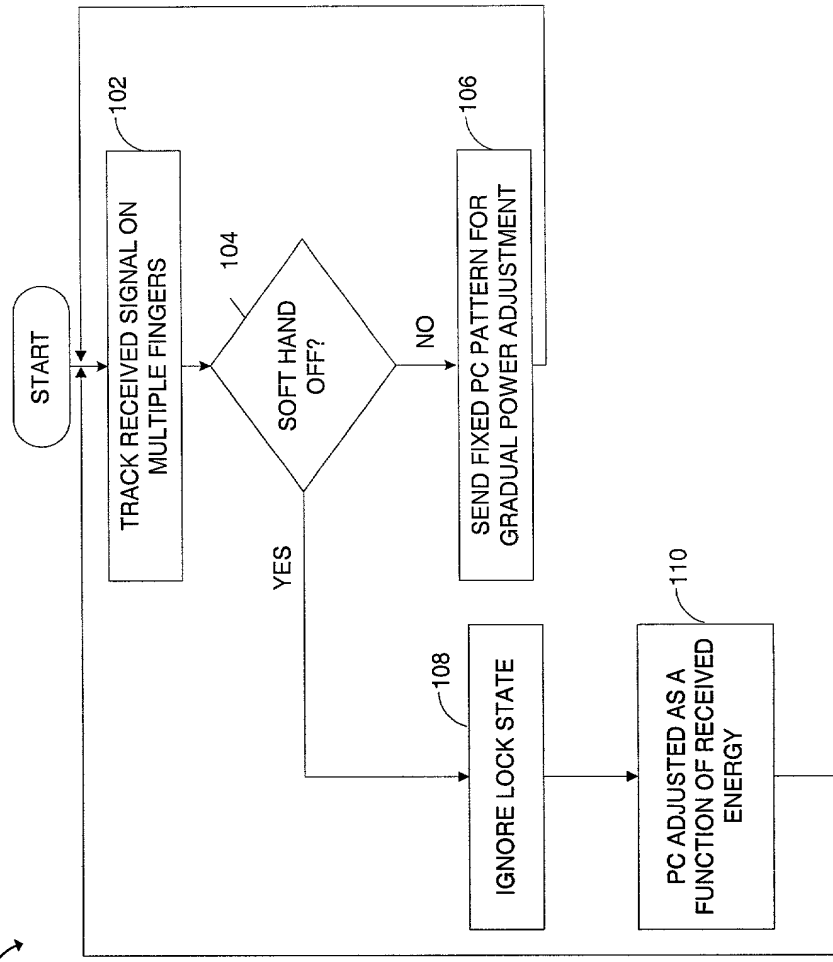


FIG. 7

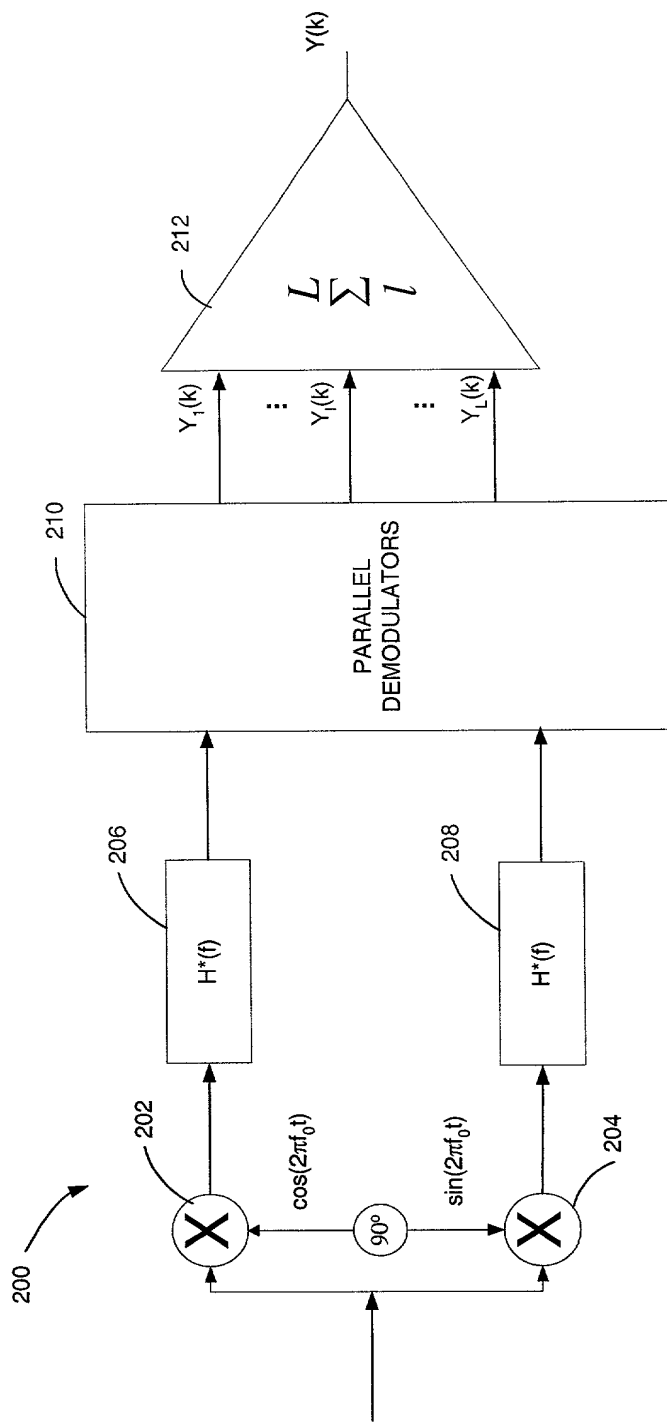


FIG. 8